

RAL920000108US1

PATENT

-1-

UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	:	Group Art Unit:
Franc E. Noel et al.	:	
	:	
Serial No.:	:	
	:	
Filed:	:	
(herewith)	:	
	:	IBM Corporation
Title: INTEGRATION OF DIGITAL	:	Intellectual Property Law
SIGNAL PROCESSOR	:	3039 Cornwallis Road
	:	Research Triangle Park, NC 27709

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

This Information Disclosure Statement is being submitted in connection with the above-identified application for patent. Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. § 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 C.F.R. § 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 C.F.R. § 1.56(a) exists.

U.S. PAT. & TM. OFF.
10/006511
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RAL920000108US1

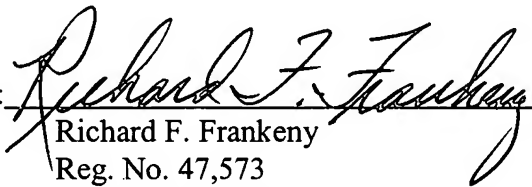
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The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 C.F.R. § 1.98(a)(1).

A copy of each of the items identified on the attached Form PTO-1449 is supplied herewith, except for the pending patent applications, for which no copies are being submitted.

Respectfully submitted,

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In Place of FORM PTO-1449 (Modified)

Serial No.:

Applicants: Franc E. Noel et al.

Filing Date: (herewith)

Group:

Atty. Docket No.: RAL920000108US1

**LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANTS' INFORMATION DISCLOSURE
STATEMENT**

JP0658 U.S. PTO
10/006511
12/05/01

Reference Designation

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
AAA	5,546,547	08/13/96	Bowes et al.	395	294	
ABC	5,502,719	03/26/96	Grant et al.	370	58.2	
ACA	6,055,591	04/25/00	Nordling	710	69	
ADA	5,519,640	05/21/96	Ganesan et al.	364	514	
AEA	5,799,067	08/25/98	Kikinis et al.	379	93.06	
AFA	5,590,284	12/31/96	Crosetto	395	200.05	
AGA	5,251,208	10/05/93	Canniff et al.	370	77	
AHA	6,065,131	05/16/00	Andrews et al.	713	600	
AIA	5,610,912	03/11/97	Johnston	370	359	
AJA	5,604,740	02/18/97	Pinault et al.	370	463	
AKA						
ALA						
AMA						
ANA						
AOA						
APA						

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
AQA	WO0116777A1	03/08/01	PCT			X
ARA						
ASA						
ATA						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner Initial

AUA M. Bombana et al. "IP-Based Design of Custom Field Programmable Network Processors," *Electronics, Circuits and Systems, 1998 IEEE Interr Conference*, Vol. 1, September 1998, pp. 467-471.

AVA "Synchronization of Multi-Media Digital Signal Processor with Integrated Services Digital Network Timing," *IBM Technical Disclosure Bulletin*, Vol. 37, No. 9, September 1994, pp. 9-11.

Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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